Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	384	parallel near3 load$3 with enable	US-PGPUB; USPAT	AND	OFF	2005/11/29 11:28
S2	1	parallel near3 load$3 with enable same configuration near2 data	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:38
S3	288	parallel with load$3 near2 configuration	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:38
S4	0	parallel with load$3 near2 configuration same enable near2 line	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:39
S5	11	parallel with load$3 near2 configuration same enable	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:41
S6	95	configuration near2 block with enable	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:45
S7	829	block with enable FPGA	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:45
S8	0	block with enable near2 load$3FPGA	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:45
S9	8	block with enable near2 load$3 FPGA	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:47
S10	2	parallel near3 load$3 with enable near2 block	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:48
S11	384	parallel near3 load$3 with enable	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:48
S12	0	parallel near3 load$3 with enable same configuration near2 controller	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:48
S13	2	parallel near3 load$3 with enable configuration near2 controller	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:49
S14	16	parallel near3 load$3 with enable FPGA	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:50
S15	119	enable near3 load$3 near6 block$1	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:50
S16	9	enable near3 load$3 near6 block$1 FPGA	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:52
S17	611	enable near2 (line$1 OR signal$1) with (configuration OR memory) near2 block$1	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:53
S18	5	enable near2 (line$1 OR signal$1) with (configuration OR memory) near2 block$1 same FPGA	US-PGPUB; USPAT	AND	OFF	2005/11/29 09:57
S19	6	block near2 enable with load$3 near6 configuration	US-PGPUB; USPAT	AND	OFF	2005/11/29 10:07
S20	4	("5705938" | "5862097" | "6130550").PN. OR ("6501677"). URPN.	US-PGPUB; USPAT; USOCR	AND	OFF	2005/11/29 09:59

S21	121	enable near2 load$3 near8 block	US-PGPUB; USPAT; USOCR	AND	OFF	2005/11/29 10:00
S22	134	enable near2 load$3 near8 block$1	US-PGPUB; USPAT; USOCR	AND	OFF	2005/11/29 10:00
S23	9	enable near2 load$3 near8 block$1 FPGA	US-PGPUB; USPAT; USOCR	AND	OFF	2005/11/29 10:00
S24	367	FPGA with load$3 near4 configuration	US-PGPUB; USPAT; USOCR	AND	OFF	2005/11/29 10:01
S25	590	FPGA same load$3 near4 configuration	US-PGPUB; USPAT; USOCR	AND	OFF	2005/11/29 10:01
S26	121	FPGA same load$3 near4 configuration block with enable	US-PGPUB; USPAT; USOCR	AND	OFF	2005/11/29 10:01
S27	79	FPGA same load$3 near4 configuration block near6 enable	US-PGPUB; USPAT; USOCR	AND	OFF	2005/11/29 10:01
S28	3	block near2 enable same load$3 near3 configuration	US-PGPUB; USPAT	AND	OFF	2005/11/29 10:08
S29	19	block near2 enable load$3 near3 configuration same FPGA	US-PGPUB; USPAT	AND	OFF	2005/11/29 10:26
S30	6	block near2 enable load$3 near3 configuration near2 block	US-PGPUB; USPAT	AND	OFF	2005/11/29 10:18
S31	5	parallel near3 load$3 same enable near2 line same configuration	US-PGPUB; USPAT	AND	OFF	2005/11/29 10:28
S32	7	parallel near3 load$3 same enable near2 load$3 same configuration	US-PGPUB; USPAT	AND	OFF	2005/11/29 10:28
S33	452	parallel near3 load$3 with configuration	US-PGPUB; USPAT	AND	OFF	2005/11/29 10:28
S34	1	parallel near3 load$3 with configuration same block near2 enable	US-PGPUB; USPAT	AND	OFF	2005/11/29 10:30
S35	1	parallel near3 load$3 with configuration same write near2 enable	US-PGPUB; USPAT	AND	OFF	2005/11/29 10:30
S36	30	parallel near3 load$3 with configuration write near2 enable	US-PGPUB; USPAT	AND	OFF	2005/11/29 10:31
S37	4	parallel near3 load$3 with configuration write near2 enable with block$1	US-PGPUB; USPAT	AND	OFF	2005/11/29 10:31
S38	3	load$3 near3 configuration with block near4 enable	US-PGPUB; USPAT	AND	OFF	2005/11/29 11:08
S39	60	enable near4 block near2 load$3	US-PGPUB; USPAT	AND	OFF	2005/11/29 11:08

S40	38	enable near4 block near2 load$3 parallel	US-PGPUB; USPAT	AND	OFF	2005/11/29 11:08
S41	303	load$3 configuration same (enable OR allow) near6 blocks	US-PGPUB; USPAT	AND	OFF	2005/11/29 11:28
S42	45	load$3 configuration same (enable OR allow) near6 blocks FPGA	US-PGPUB; USPAT	AND	OFF	2005/11/29 11:31
S43	10	("5552722" | "5914616" | "5933023" | "6049487" | "6122218" | "6262596" | "6297665" | "6373779" | "6526557" | "6538467").PN. OR ("6897676"). URPN.	US-PGPUB; USPAT; USOCR	AND	OFF	2005/11/29 11:42
S44	10	("5670897" | "5914616" | "6020758" | "6057704" | "6091263" | "6102963" | "6262596").PN. OR ("6526557").URPN.	US-PGPUB; USPAT; USOCR	AND	OFF	2005/11/29 11:35
S45	2	S43 S44 parallel	US-PGPUB; USPAT; USOCR	AND	OFF	2005/11/29 11:42
S46	16801	load$3 configuration with mode	US-PGPUB; USPAT	AND	OFF	2005/11/29 14:33
S47	381	load$3 near2 configuration with mode	US-PGPUB; USPAT	AND	OFF	2005/11/29 14:33
S48	12	load$3 near2 configuration with second near2 mode	US-PGPUB; USPAT	AND	OFF	2005/11/29 14:33
S49	8	"6496971"	US-PGPUB; USPAT	AND	OFF	2005/12/01 11:26